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APPLICATION NO.		FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/690,999 10/		10/22/2003	Sheng-Wei Yang	10113091	5671
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QUINTERO LAW OFFICE 1617 BROADWAY, 3RD FLOOR			GOUDREAU, GEORGE A		
SANTA MONICA, CA 90404				ART UNIT	PAPER NUMBER
				1763	

DATE MAILED: 09/23/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary Examiner						
Examiner		Application No.	Applicant(s)			
Goorge A. Goudreau The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 2 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. THE MAILING DATE OF THIS COMMUNICATION. If the period for reply reperiod with the property is a period of the communication of the property is a period with the property of the period with the property with the state with the property with the property with the state with the property with the property with the state with the property with		10/690,999	YANG ET AL.			
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1. Claims 1-29 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

- -The wording used in claims 1, and 16 is written in a confusing manner, and should be reworded. (i.e.-In claims 1, and 16, applicant recites that the first oxide layer is etched until it is lowered to the surface of opening in the trench. The figures used to illustrate applicant's claimed invention, show that the first dielectric layer is actually etched until it is lowered below the opening in the trench. The examiner cannot tell what applicant is trying to claim. The figures which are used to illustrate applicant's claimed invention, show the first dielectric is etched below the opening in the trench while the claims recite that the first dielectric is etched to the opening in the trench.);
- -The wording used in claims 7, 9, 21, and 23 is very confusing, and should be reworded. (The usage of the term "...in order...');
- -There should be no period in the middle of a sentence in claim 8.; and
- -The wording used in claims 8, and 22 is very confusing, and should be reworded. (The phrase "...lowered about 100-1000 angstrom..." in claims 8, and 22 is confusing.)

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2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

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A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 3. Claims 1, 5, 7, 10, and 12 –14 are rejected under 35 U.S.C. 102(e) as being anticipated by En-Ho et. al. (2003/0143852).

En-Ho et. al. disclose a process for forming an STI structure which is comprised of the following steps:

- -A Si3N4 (104)/SiO2 laminate pad layers are formed onto the surface of the Si wafer (100).;
- -A STI trench (108) is etched through the pad layers, and the top portion of the surface of the Si wafer using a patterned photo resist etch mask.;
- -A liner oxide layer (101) is formed inside the trench using a thermal oxidation process.;
- -A first oxide layer (112) is grown inside the STI trench as well as on the surface of the wafer using TEOS in a LPCVD process.;
- -The first oxide layer is etched below the surface of the STI trench using an HF wet etching step.:

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-A second oxide layer is grown inside the STI trench as well as on the surface of the wafer using TEOS in an HDPCVD process.;

- -The oxide layers are thermally annealed.; and
- -The oxide layers are planarized using a cmp polishing process.

This is discussed specifically on pages 1-3. This is shown in figures 1-2.

4. Claims 1, and 1-10 are rejected under 35 U.S.C. 102(b) as being anticipated by Kim (6,339,004).

Kim discloses a process for forming an STI structure which is comprised of the following steps:

- -A photo resist etch mask (12) is used in the patterning of a Si wafer (11) to form an STI trench.;
- -A thermal oxide layer (13) is grown in the inside walls of the STI trench.;
- -A Si3N4 layer (14) is grown on the surface of the oxide layer using a LPCVD process.;
- -The Si3N4 layer is etched to form sidewall spacers on the inside of the STI trench.;
- -A SiO2 layer (15) is grown on the Si3N4 layer using an HDPCVD process.; and
- -The SiO2 layer is cmp planarized down to the surface of the thermal oxide pad layer (16).

This is discussed in columns 1-4. This is shown in figures 2 A- 2 E.

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5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 6. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).
- 7. Claims 4, 6, 8-9, and 11 are rejected under 35 U.S.C. 103(a) as being unpatentable over En-Ho et. al. as applied in paragraph 3 above.

En-Ho et. al. As applied in paragraph 3 above fail to disclose the following aspects of applicant's claimed invention:

-the specific formation of the different layers to the specific thicknesses which are claimed by the applicant

It would have been obvious to one skilled in the art to form an STI trench with the specific aspect ratio which is claimed by the applicant based upon the following. It would have been desirable to form an STI trench with an aspect ratio such that the STI structure formed in the trench is sufficiently large to provide an adequate amount of

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insulation between adjacent devices on the surface of the wafer which are separated by the STI structure without consuming an excessive amount of expensive real estate on the wafer surface.

It would have been obvious to one skilled in the art to form the first dielectric to the specific thicknesses which are claimed by the applicant based upon the following. It would have been obvious to one skilled in the art to form the first insulating layer to a sufficient thickness to coat the bottom portion of the STI trench without forming the first insulating layer to an excessive thickness which would adversely affect production costs.

It would have been obvious to one skilled in the art to form the second dielectric to the specific thicknesses which are claimed by the applicant based upon the following. It would have been obvious to one skilled in the art to form the second insulating layer to a sufficient thickness to fill the STI trench without forming the second insulating film to an excessive thickness which would adversely affect production costs.

8. Claims 4, 7-9, and 11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kim as applied in paragraph 4 above.

Kim as applied in paragraph 4 above fail to disclose the following aspects of applicant's claimed invention:

-the specific formation of the different layers to the specific thicknesses which are claimed by the applicant

It would have been obvious to one skilled in the art to form an STI trench with the specific aspect ratio which is claimed by the applicant based upon the following. It

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would have been desirable to form an STI trench with an aspect ratio such that the STI structure formed in the trench is sufficiently large to provide an adequate amount of insulation between adjacent devices on the surface of the wafer which are separated by the STI structure without consuming an excessive amount of expensive real estate on the wafer surface.

It would have been obvious to one skilled in the art to form the first dielectric to the specific thicknesses which are claimed by the applicant based upon the following. It would have been obvious to one skilled in the art to form the first insulating layer to a sufficient thickness to coat the bottom portion of the STI trench without forming the first insulating layer to an excessive thickness which would adversely affect production costs.

It would have been obvious to one skilled in the art to form the second dielectric to the specific thicknesses which are claimed by the applicant based upon the following. It would have been obvious to one skilled in the art to form the second insulating layer to a sufficient thickness to fill the STI trench without forming the second insulating film to an excessive thickness which would adversely affect production costs.

9. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

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10. Any inquiry concerning this communication should be directed to examiner

George A. Goudreau at telephone number (571)-272-1434.

George A. Goudreau Primary Examiner

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